

A hand-drawn schematic diagram illustrating a system architecture. The diagram features three primary processing units, each represented by a square box containing the number '17'. These units are labeled 14 (top right), 10 (middle left), and 16 (bottom right). They are interconnected via a central curved structure (12) and various other components. A curved line (20) connects unit 14 to unit 10. A curved line (22) connects unit 10 to unit 16. A curved line (25) connects unit 14 to unit 16. A curved line (30) connects unit 10 to unit 14. A curved line (34) connects unit 10 to unit 16. A curved line (36) connects unit 14 to unit 16. A curved line (74) connects unit 14 to unit 16. A curved line (76) connects unit 10 to unit 16. A curved line (78) connects unit 14 to unit 16. A curved line (80) connects unit 10 to unit 16. A curved line (82) connects unit 14 to unit 16. A curved line (84) connects unit 10 to unit 16. A curved line (86) connects unit 14 to unit 16. A curved line (88) connects unit 10 to unit 16. A curved line (90) connects unit 14 to unit 16. A curved line (92) connects unit 10 to unit 16. A curved line (94) connects unit 14 to unit 16. A curved line (96) connects unit 10 to unit 16. A curved line (98) connects unit 14 to unit 16. A curved line (100) connects unit 10 to unit 16.

Fig 1

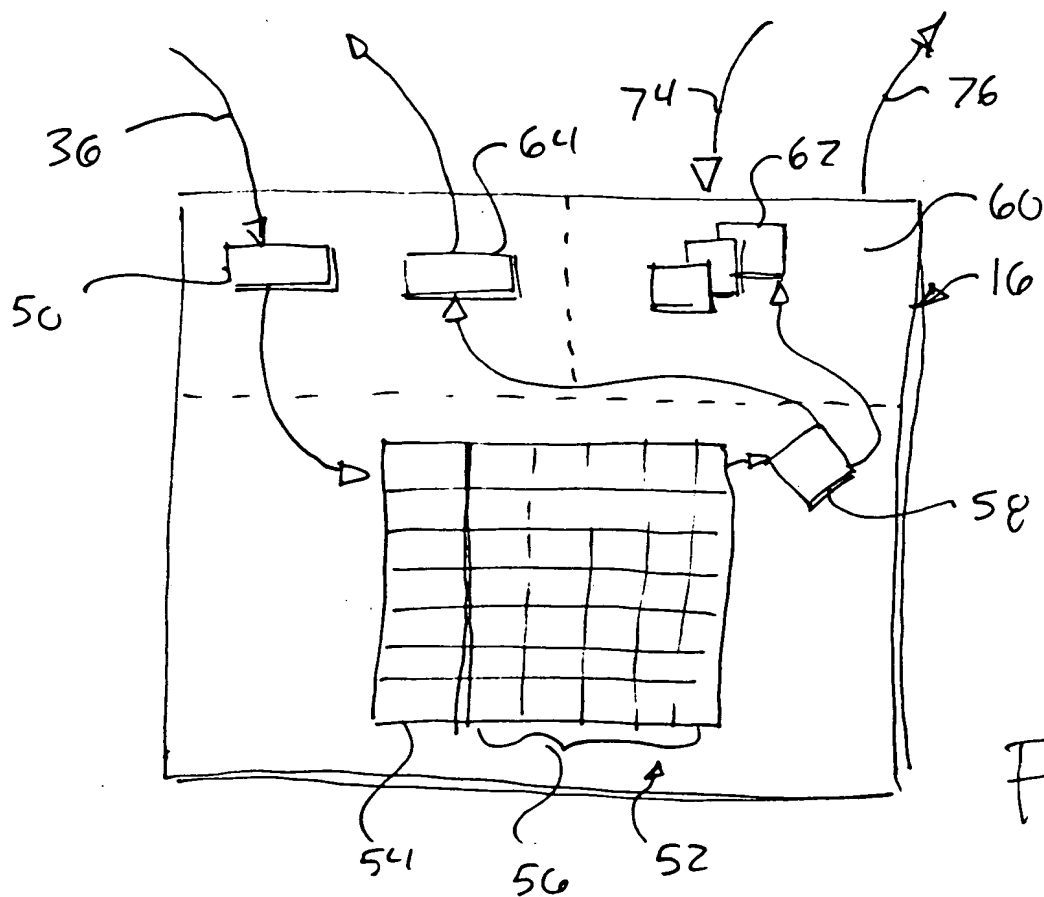


Fig 2

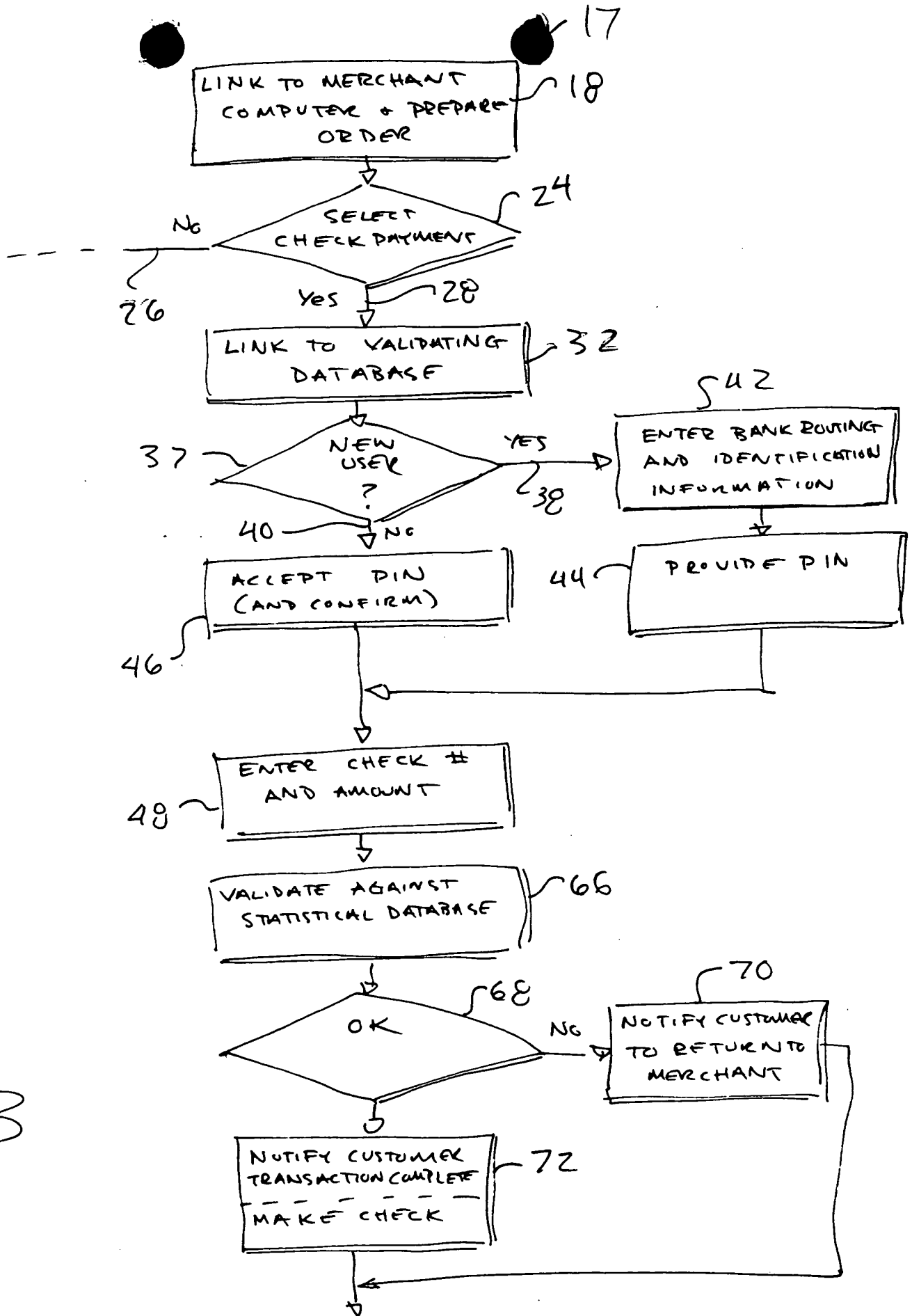


Fig 3